

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	95	"indirect VLIW"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/12/06 16:23
L2	95	L1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/12/06 16:23
L3	111	"VLIW memory"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/12/06 16:23
L4	47	extend\$4 near5 (VLIW or "very long instruction word")	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/12/06 16:24
L5	0	"VLIW address".clm.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/12/06 16:24
L6	24	"VLIW memory".clm.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/12/06 16:24
L7	0	L6 "Standard width". clm.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/12/06 16:25
S1	1631	expand\$4 near5 ("instruction set" or architecture)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/11 14:38
S2	169	"712".clas. expand\$4 near5 ("instruction set" or architecture)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/11 14:40
S3	37	(VLIW or "very long instruction word") "712".clas. expand\$4 near5 ("instruction set" or architecture)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/11 14:40

S4	45	(VLIW or "very long instruction word") expand\$4 near5 ("instruction set" or architecture)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/11 14:44
S5	31228	(VLIW or "very long instruction word") (expand\$4 near5 ("instruction set" or architecture)) or prefix	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/11 14:44
S6	153	(VLIW or "very long instruction word") ((expand\$4 near5 ("instruction set" or architecture)) or prefix)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/11 14:45
S7	53	(VLIW or "very long instruction word") ((expand\$4 near5 ("instruction set" or architecture)) or (prefix with ("instruction set" or architecture)))	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/11 14:45
S8	0	VLIW with "memory basket"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/12 09:37
S9	10	"memory basket"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/12 09:38
S10	1	"712".clas. vimb	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/12 09:38
S11	0	128x32 bit	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/12 16:38
S12	1863	"128" with ("32 bit" or 32-bit)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/12 16:38
S13	110	"712".clas. "128" with ("32 bit" or 32-bit) with register	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/12 16:39

S14	30	"712".clas. "128" with ("32 bit" or 32-bit) with "register file"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/12 16:39
S15	20	"712".clas. "128" with ("32 bit" or 32-bit) with "register file" viiw	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/14 11:26
S16	2	viw advantage with (mac or "multiply acumulate")	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/13 08:56
S17	0	advantage with "multiply acumulate"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/13 08:56
S18	1	"multiply acumulate"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/13 08:56
S19	2257	"multiply acumulate"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/13 08:56
S20	57	advantage with "multiply acumulate"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/13 09:04
S21	4	viw advantage with "multiply acumulate"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/13 08:57
S22	4	"712".clas. advantage with "multiply acumulate"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/13 09:05
S23	506	"712".clas. "multiply acumulate"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/13 09:05
S24	170	"712".clas. "multiply acumulate" near5 unit	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/13 09:05
S25	46	"712".clas. "multiply acumulate" near5 unit viw	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/13 09:05

S26	1	vliw "load indirect" mask\$4 near1 bit	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/13 10:59
S27	7	"load indirect" mask \$4 near1 bit	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/13 10:59
S28	1	"load indirect" mask \$4 near1 bit with load	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/13 10:59
S29	4	"load indirect" same mask	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2007/09/30 15:55
S30	6757	load same mask	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/14 09:46
S31	197	load with mask "712". clas.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/14 09:52
S32	6	load with mask "712". clas. load near5 indirect	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/14 09:52
S33	12	"712".clas. "128" with ("32 bit" or 32- bit) with "register file" vliw tremblay.in.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/14 11:17
S34	2	"6341348".pn.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/04/14 11:26
S35	196	fetch\$4 near2 instruction near5 "data memory"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2007/09/30 15:55
S36	121	"712".clas. fetch\$4 near2 instruction near5 "data memory"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2007/09/30 15:55
S37	6	"712".clas. fetch\$4 near2 instruction near5 "data memory" VLIW	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2007/09/30 15:56

S38	16	"712".clas. fetch\$4 near2 instruction\$1 near5 "data memory" VLIW	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2007/09/30 15:59
S39	8	"712".clas. fetch\$4 adj2 instruction\$1 near5 "data memory" VLIW	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2007/09/30 15:56
S40	2	"6173389".pn.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2007/09/30 16:09
S41	1	"6173389".pn. indirect \$4 with advantag\$4	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2007/09/30 16:09
S42	2	"6957321".pn.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/06/06 12:05
S43	171	variable near5 VLIW	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/06/19 10:53
S44	129	variable near5 length near5 VLIW	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/06/19 10:54
S45	2	"6397324".pn.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/06/19 11:10
S46	10	"variable length" with "vliw instructions"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/06/19 11:11
S47	10	"variable length" with "VLIW instructions"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/06/19 13:31
S48	90	"indirect VLIW"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/06/19 14:04

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